

Amendments to the Specification:

On page 1, after the Title, before line 1, please insert the following paragraph:

CROSS-REFERENCE TO RELATED APPLICATION

This application is a continuation of U.S. Application Ser. No. 10/191,926, which is a continuation of U.S. Patent Application Serial No. 09/623,052, filed January 9, 2001, each of which is expressly incorporated herein by reference in its entirety.

Please replace the paragraph beginning at Page 1, line 4 with the following amended paragraph:

The related art on which this patent specification is based is described in German Patent Application 196 54 846.2-53 (Method of automatic dynamic reloading of dataflow processors (DFPs) and modules having a two- or multidimensional programmable cell matrix (FPGAs, DPGAs, etc.) and in German Patent Application 196 54 593. 5-53 (Run-time reconfiguration method for programmable modules). A method of configuring and reconfiguring DFPS, as well as FPGAs, DPGAs and similar modules according to the related art in which a separately configured central higher-order microcontroller-like module assumes the task of distribution of configuration data to a plurality of lower-order, mostly passive control units is described in these documents.

Please replace the paragraph beginning at Page 3, lines 30-33 with the following amended paragraph:

-if KR3 is loaded but KR4 is not, and KR5 ~~is~~, KR4 must be pre-marked so that it has the highest priority in a subsequent loading sequence, which means high overhead.

Please replace the paragraph beginning at page 7, line 31 with the following amended paragraph:

The configurations to be loaded are prioritized by the fact that the CT runs through the FILMO memory before the configuration to be actually loaded is processed. A FIFO-like structure of the FILMO ensures that KWs which could not be fully processed during previous trigger requests automatically obtain a higher priority with respect to the new {KW} to be processed. When the FILMO memory is processed each

Please replace the paragraph beginning at page 37, line 5 with the following amended paragraph:

Figures 12c, d: Method for sending a ~~{incomplete}~~ trigger vector

Please replace the paragraph beginning at page 37, line 18 with the following amended paragraph:

Figure 18: Deadlock problem during the configuration of a two-dimensional array ~~(see patent description)~~

Please replace the paragraph beginning at page 46, line 25 with the following amended paragraph:

One of the pointers is sent to the CTR address input via a multiplexer (1001). The data arrives at the CTR via a multiplexer (1002) either from the higher level CT (1005) or from a register (1003). The data from the higher level CT or the CTR is forwarded to the state machine and the FILMO (1006) via a multiplexer (1004). If a REMOVE command occurs, the data is sent from the higher level CT directly to the state machine via 1004, while otherwise the commands are sent from the CTR to the state machine ~~{German text garbled}~~. Register 1003 is used for storage and feedback of commands, shifted from one address to another during one pass of the Garbage Collector, to the CTR input.